

Enhancement of ATC by Optimal Allocation of TCSC and SVC by Using Genetic Algorithm

Sandhya Rani Bhooma , Suresh Regatti

*Student in EEE Department Assistant Professor in EEE Department Sri Venkateshwara Engineering College
,Suryapet*

Abstract: *This paper proposes Improving of ATC is an important issue in the current de-regulated environment of power systems. The Available Transfer Capability (ATC) of a transmission network is the unutilized transfer capabilities of a transmission network for the transfer of power for further commercial activity, over and above already committed usage. Power transactions between a specific seller bus/area and a buyer bus/area can be committed only when sufficient ATC is available. Transmission system operators (TSOs) are encouraged to use the existing facilities more effectively to enhance the ATC margin. Heavily loaded circuits and buses with relatively low voltages can limit ATC usually. It is well known that FACTS technology can control voltage magnitude, phase angle and circuit reactance. Using these devices may redistribute the load flow, regulating bus voltages. Therefore, it is worthwhile to investigate the impact of FACTS controllers on the ATC. In this thesis focuses on the evaluation of the impact of TCSC and SVC as FACTS devices on ATC and its enhancement during with and without line outage cases. In a competitive (deregulated) power market, optimal the location of these devices and their control can significantly affect the operation of the system and will be very important for ISO.*

Keywords: *FACTS devices, Available Transfer Capability, TCSC, Genetic Algorithm*

I. Introduction

Deregulation and restructuring of the electric power industry is occurring in many nations throughout the world. Competitive marketplace is established for market participants who will encounter many new problems in market operation and regulation. In the US, the Federal Energy Regulatory Commission (FERC) gave the right of nondiscriminatory open access to the transmission facilities to all market participants. It is (ATC) information be made available on a publicly Accessible Open Access Same-time Information required that so-called Available Transfer Capability System (OASIS) [1]. ATC is defined by North American Electric Reliability Council (NAERC) as a measure of the transfer capability in the physical transmission network for transfers for future commercial activities over already committed uses. The calculation of ATC involves three components: Total Transfer Capability (TTC), Transmission Reliability Margin (TRM) and Capacity Benefit Margin (CBM) [1]. Based on a base case, TTC is the largest flow increase between the selected source/sink transfer without any line thermal overload, violation of voltage limits, voltage collapse or transient instability. The calculation of TTC is the major burden during the calculation of ATC. TTC is dependent on many factors, such as the base case of system operation, system operation limits, network configuration, specification of contingencies, etc. FACTS devices, which can provide direct and flexible control of power transfer, can be very helpful in the operation of competitive power markets. How to use FACTS to control power flow control to improve power transfer capability so that it enhance the power market competitiveness is an active research area. The compensation scheme of TCSC has been widely accepted as a solution for the limitation created by generation and transmission systems. With proper control of FACTS devices, TTC may be adjusted significantly. Thyristor Controlled Series Compensator (TCSC) as an effective series compensation device can be used for this purpose. In this paper the effectiveness of TCSC to enhance TTC at steady state is investigated. The location and the amount of compensation will have to be determined in power flow control problems. Several studies have evaluated the function of series compensators using objective sensitivities for the predetermined locations or parameters of the device required [2,3]. Genetic Algorithms (GAs) are probabilistic heuristic search procedures based on natural genetic system. It uses probabilistic transition rules, not deterministic rules. GA is highly multi-direction, pmalle and rather robust method in searching global optimal solution of complex optimization problems. By using random choice as a tool in the search process, it possesses the advantage to help preventing the algorithm getting stuck in a local minimum. Power system researchers have implemented the technique broadly in recent years [4]. This paper explores the application of Genetic Algorithm (GA) to determine the location and the amount of compensation of TCSC for enhancing TTC. A simplified TTC has been used as an index to evaluate the impacts of TCSCS in competitive power markets and then a real Genetic Algorithm is developed to explore the allocations and the amount of compensation of one and two TCSCS in a system. This paper is organized as follows. Section 2 discusses the calculation of Total Transfer

Capability. TCSC modeling is outlined in Section 3. Section 4 presents the procedure for constructing Genetic Algorithm and applying different techniques for GA operators. Studies on the IEEE 14-bus to demonstrate the implementation of proposed algorithm are presented in Section 5.

II. Available Transfer Capability

Mathematically, ATC is defined as [4, 5], the Total Transfer Capability (TTC) less the Transmission Reliability Margin (TRM), less the Capacity Benefit Margin (CBM) and the sum of existing transmission commitments (TC) which includes retail customer service. Transmission Reliability Margin (TRM) [4, 5] is defined as that amount of transmission transfer capability necessary to ensure that the interconnected transmission network is secure under a reasonable range of uncertainties in system conditions. Capacity Benefit Margin (CBM) [4, 5] is defined as that amount of transmission transfer capability reserved by load serving entities to ensure access to generation from interconnected systems to meet generation reliability requirements.

$$ATC = TTC - TRM - CBM - TC$$

In this paper, the margins such as TRM and CBM are not considered. Therefore ATC here can be expressed as:
 $ATC = TTC - TC$

The procedure proposed involves the method based on multiple load flow runs AC load flow for each increment of transaction between an interface and checks whether any of the operating conditions such as line flow limit or bus voltage limit is violated. The minimum out of the two critical transaction values is taken as the TTC for the system in that condition.

Algorithm for ATC Calculation Using CPF

(i) a) Read the system line data and bus data

System data: From bus, To bus, Line resistance, Line reactance, half line charging, Off nominal turns ratio, maximum line flows.

Bus data: Bus no, Bus type, Pgen, Qgen, PLoad, QLoad, Pmin, Pmax, Vsp shunt capacitance data.

b) Cal Pshed(i), Qshed(i), for i=1 to n

Where Pshed(i)= Pgen(i)-PLoad(i)

Qshed(i)= Qgen(i)-QLoad(i)

c) Form Ybus using sparsity technique

(ii) a) iter=1 iteration count

b) Set $|\Delta P_{\max}| = 0$ and $|\Delta Q_{\max}| = 0$

$$c) \text{ Calculate } P_{\text{cal}}(i) = \sum_{q=1}^n |V_i| |V_q| |Y_{iq}| \cos(\delta_{iq} - \theta_{iq})$$

$$Q_{\text{cal}}(i) = \sum_{q=1}^n |V_i| |V_q| |Y_{iq}| \sin(\delta_{iq} - \theta_{iq})$$

d) Calculate

$$P(i) = P_{\text{shed}}(i) - P_{\text{cal}}(i)$$

$$Q(i) = Q_{\text{shed}}(i) - Q_{\text{cal}}(i) \text{ for } i=1 \text{ to } n$$

Set $P_{\text{slack}}=0.0$, $Q_{\text{slack}}=0.0$

e) Calculate $|\Delta P_{\max}|$ and $|\Delta Q_{\max}|$ form $[\Delta p]$ and $[\Delta Q]$ vectors

f) Is $|\Delta P_{\max}| \leq \epsilon$ and $|\Delta Q_{\max}| \leq \epsilon$

If yes go to step (vii), problem converged case

iii) Form Jacobian elements

a) Initialize $A[i][j]=0$, for $i=1$ to $2n+2$, $j=1$ to $2n+2$ b) Form diagonal elements for $i=1$ to n

$$H_{pp} = \frac{\partial P_p}{\partial \delta_p} = -Q_p - B_{pp} |V_p|^2$$

$$N_{pp} = \frac{\partial P_p \cdot |V_p|}{\partial V_p} = P_p + G_{pp} |V_p|^2$$

$$M_{pp} = \frac{\partial Q_p}{\partial \delta_p} = P_p - G_{pp} |V_p|^2$$

$$L_{pp} = \frac{\partial Q_p \cdot |V_p|}{\partial V_p} = Q_p - B_{pp} |V_p|^2$$

c) Formation of off diagonal elements

$$H_{pq} = \frac{\partial P_p}{\partial \delta_q} = |V_p||V_q|(G_{pq} \sin \delta_{pq} - B_{pq} \cos \delta_{pq})$$

$$N_{pq} = \frac{\partial P_p}{\partial V_q} |V_q| = |V_p||V_q|(G_{pq} \cos \delta_{pq} + B_{pq} \sin \delta_{pq})$$

$$M_{pq} = \frac{\partial Q_p}{\partial \delta_q} = -N_{pq}$$

$$L_{pq} = \frac{\partial Q_p}{\partial V_q} |V_q| = H_{pq}$$

d) Modification of Jacobian elements for slack bus and generator buses For slack bus

$$H_{pp} = 10^{20}, L_{pp} = 10^{20} \text{ For PV buses } L_{pp} = 10^{20}$$

e) Form right hand side vector

$$B[i] = \Delta P[i], B[i+n] = \Delta Q[i]$$

for i=1 to n Jacobian correction mismatch vector

(iv) Use Gauss-elimination method for solving

$$[A][\Delta X] = [B] \text{ Update the phase angle and voltage magnitudes for } i=1 \text{ to } n$$

$$\delta_i = \delta_i + \Delta X_i$$

$$V_i = V_i + \{\Delta X_{i+n}\} V_i$$

(v) One iteration over Advance iteration count iter=iter+1 If (iter>itermax) go to step (ii) (b) Else go to step (vi).

(vi) NR is not converged in “itermax” iterations

(vii) NR is converged in ‘iter’ iterations calculate

a. Line flows b. Bus powers, Slack bus power.

c. Print the converged voltages, line flows and powers.

(viii) Read the sending bus (seller bus) *m* and the receiving bus (buyer bus) *n*.

(ix) Assume some positive real power injection change Δtp (=0.1) i.e. λ -factor at seller bus-*m* and negative injection Δtp (=0.1) i.e. λ -factor at the buyer bus-*n* and form mismatch vector.

(x) Repeat the load flow (i.e., from steps (ii) to (vii)) and from the new line flows check whether any of the line is overloaded. If yes stop the repeated power flow else go to (ix).

(xi) The maximum possible increment achieved above base-case load at the sink bus is the ATC.

III. Modeling of TCSC

Transmission lines are represented by lumped π equivalent parameters. The series compensator TCSC is simply a static capacitor/reactor with impedance jx_c [6]. Fig. shows a transmission line incorporating a TCSC.

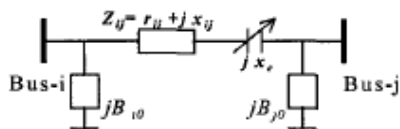


Fig: Equivalent circuit of a line with TCSC

Where X_{ij} is the reactance of the line, R_{ij} is the resistance of the line, B_{i0} and B_{j0} are the half-line charging susceptance of the line at bus-i and bus-j. The difference between the line susceptance before and after the addition of TCSC can be expressed as:

$$\Delta y_{ij} = y'_{ij} - y_{ij} = (g'_{ij} + jb'_{ij}) - (g_{ij} + jb_{ij})$$

$$g_{ij} = \frac{r_{ij}}{\sqrt{r_{ij}^2 + x_{ij}^2}}, \quad b_{ij} = -\frac{x_{ij}}{\sqrt{r_{ij}^2 + x_{ij}^2}}$$

$$g'_{ij} = \frac{r_{ij}}{\sqrt{r_{ij}^2 + (x_{ij} + x_c)^2}}, \quad b'_{ij} = -\frac{x_{ij} + x_c}{\sqrt{r_{ij}^2 + (x_{ij} + x_c)^2}}$$

After adding TCSC on the line between bus i and bus j of a general power system, the new system admittance matrix Y'_{bus} can be updated as

$$Y'_{bus} = Y_{bus} + \begin{bmatrix} 0 & 0 & 0 & \dots & 0 & 0 & 0 \\ 0 & \Delta y_{ij} & 0 & \dots & 0 & -\Delta y_{ij} & 0 \\ 0 & 0 & 0 & \dots & 0 & 0 & 0 \\ \dots & \dots & \dots & \dots & 0 & \dots & 0 \\ 0 & 0 & 0 & \dots & 0 & 0 & 0 \\ 0 & -\Delta y_{ij} & 0 & \dots & 0 & \Delta y_{ij} & 0 \\ 0 & 0 & 0 & \dots & 0 & 0 & 0 \end{bmatrix} \begin{matrix} \\ \text{row-i} \\ \\ \\ \text{row-j} \\ \\ \end{matrix}$$

$\text{col-i} \qquad \qquad \text{col-j}$

Algorithm for Newton Raphson power flow Using TCSC

(a) Read the system line data, bus data and TCSC data

Line data: From bus, To bus, line resistance, line reactance, half-line charging susceptance and off nominal tap ratio.

Bus data: Bus no, Bus itype, P_{gen}, Q_{gen}, P_{load}, Q_{load}, and shunt capacitor data.

TCSC data: TCSC Line no., variable reactance (X_c)

(b) Form Y_{bus} using sparsity technique.

(c) Modify the Y_{bus} elements with the value of TCSC reactance.

The difference between the line susceptance before and after the addition of TCSC between bus-i and bus-j can be expressed as:

$$\Delta y_{ij} = y'_{ij} - y_{ij} = (g'_{ij} + jb'_{ij}) - (g_{ij} + jb_{ij})$$

$$g_{ij} = \frac{r_{ij}}{\sqrt{r_{ij}^2 + x_{ij}^2}}, \quad b_{ij} = -\frac{x_{ij}}{\sqrt{r_{ij}^2 + x_{ij}^2}}$$

$$g'_{ij} = \frac{r_{ij}}{\sqrt{r_{ij}^2 + (x_{ij} + x_c)^2}}, \quad b'_{ij} = -\frac{x_{ij} + x_c}{\sqrt{r_{ij}^2 + (x_{ij} + x_c)^2}}$$

Modification in diagonal elements as $Y_{pp}(i) = Y_{pp}(i) + \Delta y_{ij}$

Modification in off-diagonal elements as $y_{line}(ij) = y_{line}(ij) - \Delta y_{ij}$

2.(a)k₁=1 iteration count

(b)Set $|\Delta P_{max}|=0.0, |\Delta Q_{max}|=0.0$

(c)Cal P_{shed}(i),Q_{shed}(i), for i=1 to n.

Where $P_{shed}(i) = P_{gen}(i) - P_{load}(i)$

$Q_{shed}(i) = Q_{gen}(i) - Q_{load}(i)$

(d)Calculate $P_{cal}(i) = \sum_{q=1}^n |V_i| |V_q| |Y_{iq}| \cos(\delta_{iq} - \theta_{iq})$

$$Q_{cal}(i) = \sum_{q=1}^n |V_i| |V_q| |Y_{iq}| \sin(\delta_{iq} - \theta_{iq})$$

(e) Calculate $\Delta P(i) = P_{shed}(i) - P_{cal}(i)$

$\Delta Q(i) = Q_{shed}(i) - Q_{cal}(i)$ for i=1 to n

Set $\Delta P_{slack}=0.0, \Delta Q_{slack}=0.0,$

(f) Calculate $|\Delta P_{max}|$ and $|\Delta Q_{max}|$ form $[\Delta p]$ and $[\Delta Q]$ vectors

(g)Is $|\Delta P_{max}| \leq \epsilon$ and $|\Delta Q_{max}| \leq \epsilon$ If yes, go to step no. 6

3.Form Jacobian elements:

(a)Initialize A[i][j]=0.0 for i=1 to 2n, j=1 to 2n

(b) Form diagonal elements H_{pp}, N_{pp}, M_{pp} & L_{pp}

(c) Form off – diagonal elements: H_{pq}, N_{pq}, M_{pq} & L_{pp}

(d) Form right hand side vector (mismatch vector)

B[i]= $\Delta P[i]$, B[i+n]= $\Delta Q[i]$ for i=1 to n Modify the elements For p=slack bus; H_{pp}=1e20=10²⁰, L_{pp}=1e20=10²⁰,

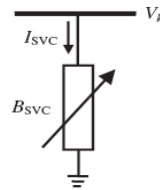
4.Use Gauss – Elimination method for following $[A] [\Delta X] = [B]$ Update the phase angle and voltage magnitudes i=1 to n For itype=1 & 2, calculate $\delta_i = \delta_i + \Delta X_i$ & $V_i = V_i + \{\Delta X_{(i+n)}\} V_i$

5.One iteration over Advance iteration count k₁=k₁+1 If (k₁< itermax) then goto step 2(b) else print problem is not converged in “itermax” iterations, Stop.

6.Print problem is converged in ‘iter’no. of iterations. a)Calculate line flowsb)Bus powers, Slack bus power.

C)Print the converged voltages, line flows and powers.

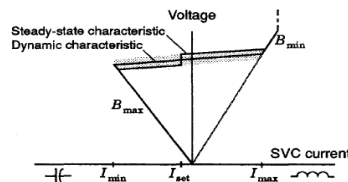
Modeling of SVC: The shunt compensator SVC is simply a static capacitor/reactor with susceptance B_{svc} [7]. Fig.shows the equivalent circuit of the SVC can be modeled as a shunt-connected variable susceptance B_{svc} at bus-i.



Variable shunt susceptance.

The reactive power injected into the bus due to SVC can be expressed as $Q_{svc} = B_{svc} V^2$

Where V is the voltage magnitude of the bus at which the SVC is connected. Fig. 3.5 shows the steady-state and dynamic voltage-current characteristics of the SVC portion of the system. In the active control range, current/susceptance and reactive power is varied to regulate voltage according to a slope (droop) characteristic. The slope value depends on the desired voltage regulation, the desired sharing of reactive power production between various sources, and other needs of the system. The slope is typically 1-5%. At the capacitive limit, the SVC becomes a shunt capacitor. At the inductive limit, the SVC becomes a shunt reactor (the current or reactive power may also be limited). The response shown by the dynamic characteristic is very fast (few cycles) and is the response normally represented in transient stability simulation. Some SVCs have a susceptance/current/reactive power regulator to slowly return the SVC to a desired steady-state operating point. This prevents the SVC from drifting towards its limits during normal operating conditions, preserving control margin for fast reaction during disturbances. During normal operation, voltage is not regulated unless the voltage exceeds a dead band determined by the limits on the output of the susceptance regulator.



SVC static characteristics at high voltage bus.

After adding SVC at bus-i of a general power system, the new system admittance matrix Y'_{bus} can be updated as [8]:

$$Y'_{bus} = Y_{bus} + \begin{bmatrix} 0 & 0 & 0 & \dots & 0 & 0 & 0 \\ 0 & Y_{shunt} & 0 & \dots & 0 & 0 & 0 \\ 0 & 0 & 0 & \dots & 0 & 0 & 0 \\ \dots & \dots & \dots & \dots & \dots & \dots & \dots \\ 0 & 0 & 0 & \dots & 0 & 0 & 0 \\ 0 & 0 & 0 & \dots & 0 & 0 & 0 \\ 0 & 0 & 0 & \dots & 0 & 0 & 0 \end{bmatrix} \begin{matrix} \text{row}-i \\ \\ \\ \\ \text{row}-j \\ \\ \end{matrix}$$

$\begin{matrix} \text{col}-i & \text{col}-j \end{matrix}$

For constant active power flow and supply voltage of V_{rms} , the required capacitive VAR is the difference between the pre compensation VAR and the required compensated VAR as given by equation [14]:

$$VAR(\text{capacitive}) = VAR(\text{required}) - VAR(\text{Uncompensated})$$

The amount of the capacitive susceptance B_{cap} is then given by Eqn.

$$B_{cap} = \frac{VAR(\text{Required}) - VAR(\text{Uncompensated})}{V_{ms}^2} \quad \text{Siemens}$$

From which the required capacitance value in Farad is given by using equation $C \text{ (Farad)} = \frac{B_{cap}}{(2\pi f)}$

Algorithm for Newton Raphson power flow Using SVC

1.(a) Read the system line data, bus data and SVC data

Line data: From bus, to bus, line resistance, line reactance, half-line charging Susceptance and off nominal tap ratio.

Bus data: Bus no, Bus itype, P_{gen} , Q_{gen} , P_{load} , Q_{load} , and Shunt capacitor data.

SVC data: SVC Bus no., variable susceptance (B_{svc})

(b)Form Y_{bus} using sparsity technique.

(c) Modify the Y_{bus} elements with the value of SVC reactance. When SVC is placed at i th bus, the modification in diagonal element as $Y_{pp}(i) = Y_{pp}(i) + y_{shunt}$

2.(a) $k_1=1$ iteration count

(b) Set $|\Delta P_{max}|=0.0, |\Delta Q_{max}|=0.0$

(c) Cal $P_{shed}(i), Q_{shed}(i)$, for $i=1$ to n .

Where $P_{shed}(i) = P_{gen}(i) - P_{load}(i)$

$Q_{shed}(i) = Q_{gen}(i) - Q_{load}(i)$

(d) Calculate $P_{cal}(i) = \sum_{q=1}^n |V_i| |V_q| |Y_{iq}| \cos(\delta_{iq} - \theta_{iq})$

$Q_{cal}(i) = \sum_{q=1}^n |V_i| |V_q| |Y_{iq}| \sin(\delta_{iq} - \theta_{iq})$

(e) Calculate $\Delta P(i) = P_{shed}(i) - P_{cal}(i)$

$\Delta Q(i) = Q_{shed}(i) - Q_{cal}(i)$ for $i=1$ to n

Set $\Delta P_{slack}=0.0, \Delta Q_{slack}=0.0,$

(f) Calculate $|\Delta P_{max}|$ and $|\Delta Q_{max}|$ form $[\Delta p]$ and $[\Delta Q]$ vectors

(g) Is $|\Delta P_{max}| \leq \epsilon$ and $|\Delta Q_{max}| \leq \epsilon$ If yes, go to step no. 6

3. Form Jacobian elements:

(a) Initialize $A[i][j]=0.0$ for $i=1$ to $2n, j=1$ to $2n$

(b) Form diagonal elements H_{pp}, N_{pp}, M_{pp} & L_{pp} (c) Form off – diagonal elements: H_{pq}, N_{pq}, M_{pq} & L_{pp}

(d) Form right hand side vector (mismatch vector)

$B[i]=\Delta P[i], B[i+n]=\Delta Q[i]$ for $i=1$ to n

(e) Modify the elements For $p=slack$ bus; $H_{pp}=1e20=10^{20}; L_{pp}=1e20=10^{20};$

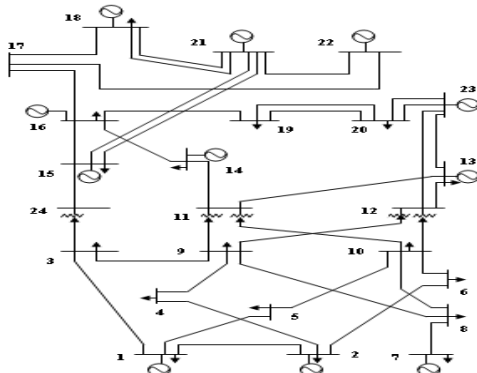
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5. One iteration over Advance iteration count $k_1=k_1+1$ If $(k_1 < itermax)$ then goto step 2(b) else print problem is not converged in “itermax” iterations, Stop.

6. Print problem is converged in ‘iter’no. of iterations. a. Calculate line flows b. Bus powers, Slack bus power. c. Print the converged voltages, line flows and powers.

IV. Case Study And Results

IEEE 24-bus system reproduced in Fig. 4 has been used to study the implementation of the proposed GA. System data can be found in [8]. Here we represent the two parallel lines connected bus 1 with bus 2 as one line. Line ratings are given in appendix A. The voltage magnitude limits of all buses are set from 0.95 to 1.1 pu



The Available Transfer Capability (ATC) are computed for a set of source/sink transfers using Continuous Power Flow (CPF). Table shows the ATCs for IEEE 24-bus system without FACTS device.

Table:-ATC without FACTS Device

Source/Sink bus no.	ATC(M.W)	Violation Constraint
23/15	770.0	Line-24 overflow
22/9	395.0	Line-38 overflow
22/5	260.0	Line-38 overflow
21/6	105.0	Line-10 overflow
18/5	260.0	Line-38 overflow

The Available Transfer Capability (ATC) are computed for a set of source/sink transfers using Continuous Power Flow (CPF), when line-8 is physically removed from the system that is connected between bus-4 and bus-9. Fig. Shows a graph voltage profile for the IEEE 24-bus system with and without outage cases.

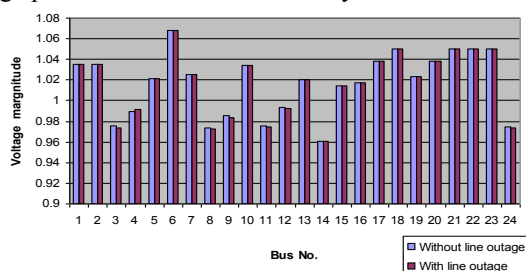


Fig:-Bus voltage profile for without and with line outage cases

V. Conclusions

In deregulated power systems, available transfer capability (ATC) analysis is presently a critical issue either in the operating or planning because of increased area interchanges among utilities. Sufficient ATC should be guaranteed to support free market trading and maintain an economical and secure operation over a wide range of system conditions. However, tight restrictions on the construction of new facilities due to the increasingly difficult economic, environmental, and social problems, have led to a much more intensive shared use of the existing transmission facilities by utilities and independent power producers (IPPs). Based on operating limitations of the transmission system and control capabilities of FACTS technology, technical feasibility of applying FACTS devices to boost ATCs are analyzed and identified.

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BIOGRAPHIE



Suresh Regatti is B.Tech(Electrical) M.tech(Electrical) he is Working as a Assistant Professor at Sri Venkateshwara Engineering College ,Suryapet, nalgonda, andrapradesh, India.His research Interests in PowerElectronics,power systems Restructuring,Distributd Generation.



Sandhya Rani Bhooma:Post graduate student in the Department of Electrical &Electronics Engineering at Sri Venkateshwara Engineering College, Suryapet, nalgonda, andrapradesh, India.Interested in PowerElectronics,power systems,FACTS,power system deregulation.