# Optimality Principle Approach For Obtaining Minimal Boolean Expression Equivalent To A Given Function 

Dr. Y. P. Sabharwal<br>Consulting Actuary<br>27 Jaina Building<br>Roshan Ara Road<br>Delhi-110007 (India)<br>Dr. Debasree Goswami<br>Associate Professor<br>Department Of Statistics<br>Hindu College<br>University Of Delhi (India)


#### Abstract

: Boolean algebra has played a significant role in the important and complicated task of designing electronic gadgetries. While it is desirable to get the minimal Boolean expressions in designing circuits, it is not necessarily uniquely available. In this paper, we present an algorithm for determining a Minimal Expression from the complete list of Prime Implicants.


Key words:Minimal Boolean Expression, Prime Implicants, Circuit design, Algorithm, Digital logic, Circuit optimization

## I. Introduction

In general, a very large number of (apparently) distinct Boolean Expressions are available for a given function $f(\underset{\sim}{x})$ defined over $\left(x_{1}, x_{2}, \ldots, x_{n}\right)$. The Electronics Engineer responsible for the implementation of $f(\underset{\sim}{x})$ looks for an expression $\Phi$ which will require the minimum number of electronic gadgetries. The requisite choice lies in $\Phi$ with the number of literals ([1],[2]). Concepts e.g., Canonical Product Term (CPT), Prime Implicant (PI), Cover Table and Irredundant Expression prove useful in this context. Algorithms, e.g. Karnaugh's Map [2], Quinnie's Rule and Birkhoff and Bartee [1], are available for obtaining the complete list of Prime Implicants. Subsequently, the adoption of Essential Prime Implicants (EPI) and Dominance Principle help in determining Irredundant Expressions and finally, the Minimal Expression(s) Equivalent to the given function. These steps, however, remain heuristic in nature. This paper presents an Algorithm for determining a Minimal Expression from the complete list of Prime Implicants.

## II. Enunciation of Birkhoff\& Bartee's Algorithm for Determining the Complete List of Prime Implicants

Let $x_{1}, x_{2}, \ldots, x_{n}$ denote the $n$ variables and $a=\left(a_{1}, a_{2}, \ldots, a_{n}\right), a_{i} \in\{0,1\}$ an assignment.
Step 1: Construct $S=\{a \mid f(a)=1\}$.
Step 2: For each $a \in S$, define the Canonical Product Term $Y=y_{1} \cdot y_{2} \cdot \ldots \cdot y_{n}$ where $y_{i}=x_{i}$ if $a_{i}=1$ and $x_{i}^{\sim}$ if $a_{1}=0$.
Then the Sum of Product Terms $\Phi=\cup_{a \in S} Y$ will be equivalent to $f$.
Remark: This can invariably be taken as the starting point, even though, one may start with any Sum of Products Expression Equivalent to $f$.
Step 3: As long as possible, effect the following
(a) If there are two product terms $\alpha$ and $\beta$ for which consensus $\gamma$ is defined and $\gamma$ is not covered by any of the product terms in $\Phi$, then add it to $\Phi$.
(b) If there are two product terms $\alpha$ and $\beta$ in $\Phi$ such that $\alpha$ covers $\beta$, then drop $\beta$ from $\Phi$.

At the end of this step each of the product terms in $\Phi$ will be a PI and no PI of $f$ will be absent from $\Phi$. Also, this Algorithm is finite.

## III. Cover Table, EPI and the Reduced Cover Table

Let $P=\left\{P_{1}, P_{2}, \ldots, P_{m}\right\}$ be the set of Prime Implicants as per Section 2 and $L_{I}=$ number of Literals in $P_{I}$.
Now develop the Cover Table and identify the EPI. Finally, determine the Reduced Cover Table by eliminating the EPI and the CPT covered by these.
Also introduce $E_{L}=1$ if $P_{L}$ is an EPI of 0 otherwise,
$C_{1}^{\prime}, C_{2}^{\prime}, \ldots C_{s}^{\prime}$ the $s$ (uncovered) CPT in this Table,
$n_{I}=$ number of non-essential PI that cover $C_{I}^{\prime}$,
$S_{I J}=L$ if $P_{L}$ is the $J$ th PI in P that covers $C_{I}^{\prime}$,
$F_{I}=F_{I-1}+n_{I}$, the cumulative sums of $n_{I} \mathrm{~s}$,
$M=\left(M_{i j}\right)_{F_{S} \times m}$ with $M_{i j}=1$,
the Control Matrix to be used for giving dynamic effect to the use of $L_{I} \mathrm{~s}$.
$D_{I J K}=0$ for $K=1,2, . ., n_{I+1}, J=1,2, \ldots, n_{I}$, and $I=1,2, \ldots, s$
to be updated later for identifying Prime Implicants for inclusion in the Minimal Expression.

## IV. The Stage Coach / Optimal Path Formulation

Let the $C_{I}^{\prime}$ correspond to the $I^{\text {th }}$ stage and $P_{S_{I J}}$ the $J^{t h}$ station at this stage. With $L_{S_{I J}}$ as the cost of including $P_{S_{I J}}$ in the Path, we have the usual Stage Coach Formulation, solvable by the Optimally Principle [2], viz. every SubPath of an Optimal path is itself Optimal. In the present context, however, inclusion of the same PI at more than one stage should contribute only once to the total number of literals. Following Algorithm will take care of this dynamic nature of the 'cost' structure.

## V. Algorithm For Optimal Path from Source To Sink Under The Dynamic Cost Structure \&Determining Therefrom A Minimal Sum of Products Expression Equivalent To The Given Function

With
$T_{I J}=$ cost of the Optimal Path from the $J^{t h}$ station of the $I^{t h}$ stage to the sink,
the Algorithm comprises of the following Steps:
Step 1: Determining the $T_{I J}$ and $S_{I J}$ and updating $M$
(a) $T_{s J}=L_{S_{S J}}$ and $M_{F_{s-1}+J S_{S J}}=0$
(b) $\quad T_{I J}=\min _{K}\left(T_{I+1 K}+M_{F_{I}+K} S_{I J} * L_{S_{I J}}\right)$

Let $K_{0}$ be the first $K$ for which the minimum value was attained

$$
\begin{aligned}
& D_{I J K_{0}}=1 \\
& M_{F_{I-1}+J L}=M_{F_{I}+K_{0} L} \text { for } L=1,2, \ldots, m \\
& M_{F_{I-1}+J S_{I J}}=0
\end{aligned}
$$

Step 2: Identifying the station with least $T_{I J}$

$$
T_{i J_{0}}=\min _{J}\left(T_{i j}\right)
$$

Step 3: The non-essential Prime Implicants to be included in the Optimal Expression
$E_{S_{i_{0}}}=2$ and $E_{S_{I+i K_{0}}}=2$ for $I=1,2, \ldots, s-1$
Step 4: The Sum of Products Expression

$$
\Phi=\bigcup_{E_{L}>0} P_{L}
$$

is a Minimal Expression (with the least number of literals) Equivalent to the given function.

## VI. Illustration

The Output depicting various aspects in respect of the function

$$
f(x)=\sum(0,2,4,5,10,11,13,15)
$$

obtained are given below in the tables.

Table - 1

| LISTING THE ENTRIES/DELETIONS WITH MODES |  |  |  |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  | MODE OF |  |  |  |  |  |  |  |
| SR.NO. | PRODUCT TERM |  |  |  | ENTRY |  |  |  |  | EXIT |  |  |
| 1 | X1~ | X2~ | X3~ | X4~ | INI | IAL |  |  |  | S( | 9 | ) |
| 2 | X1~ | X2~ | X3 | X4~ | INI | IAL |  |  |  | S( | 9 | ) |
| 3 | X1~ | X2 | X3~ | X4~ | INI | IAL |  |  |  | S( | 10 | ) |
| 4 | X1~ | X2 | X3~ | X4 | INI | IAL |  |  |  | S( | 10 | ) |
| 5 | X1 | X2~ | X3 | X4~ | INI | IAL |  |  |  | S( | 11 | ) |
| 6 | X1 | X2~ | X3 | X4 | INI | IAL |  |  |  | S( | 11 | ) |
| 7 | X1 | X2 | X3~ | X4 | INI | IAL |  |  |  | S( | 12 | ) |
| 8 | X1 | X2 | X3 | X4 | INI | IAL |  |  |  | S( | 12 | ) |
| 9 | X1~ | X2~ |  | X4~ | C( | 1 | , | 2 | ) |  |  |  |
| 10 | X1~ | X2 | X3~ |  | C( | 3 | , | 4 | ) |  |  |  |
| 11 | X1 | X2~ | X3 |  | C( | 5 | , | 6 | ) |  |  |  |
| 12 | X1 | X2 |  | X4 | C( | 7 | , | 8 | ) |  |  |  |
| 13 | X1~ |  | X3~ | X4~ | C( | 9 | , | 10 | ) |  |  |  |
| 14 |  | X2~ | X3 | X4~ | C( | 9 | , | 11 | ) |  |  |  |
| 15 |  | X2 | X3~ | X4 | C( | 10 | , | 12 | ) |  |  |  |
| 16 | X1 |  | X3 | X4 | C( | 11 | , | 12 | ) |  |  |  |

Table - 2

| COVER TABLE |  |  |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | CANONICAL PRODUCT TERMS |  |  |  |  |  |  |  |  |
| PRIME IMPLICANTS | C1 | C2 | C3 | C4 | C5 | C6 | C7 | C8 |  |
| P1 | * | * |  |  |  |  |  |  |  |
| P2 |  |  | * | * |  |  |  |  |  |
| P3 |  |  |  |  | * | * |  |  |  |
| P4 |  |  |  |  |  |  | * | * |  |
| P5 | * |  | * |  |  |  |  |  |  |
| P6 |  | * |  |  | * |  |  |  |  |
| P7 |  |  |  | * |  |  | * |  |  |
| P8 |  |  |  |  |  | * |  | * |  |

Table - 3

| PRIME IMPLICANTS TO BE INCLUDED IN THE MINIMAL EXPRESSION |  |  |  |  |  |  |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| SR. NO. | PRIME IMPLICANT |  | NO OF LITERALS | STATUS* |  |  |
| 1 | $\mathrm{X} 1 \sim$ | $\mathrm{X} 2 \sim$ |  | $\mathrm{X} 4 \sim$ | 3 | 2 |
| 2 | $\mathrm{X} 1 \sim$ | X 2 | $\mathrm{X} 3 \sim$ |  | 3 | 2 |
| 3 | X 1 | $\mathrm{X} 2 \sim$ | X 3 |  | 3 | 2 |
| 4 | X 1 | X 2 |  | X 4 | 3 | 2 |
| TOTAL |  |  |  |  |  |  |
| * 1: ESSENTIAL 2: NON-ESSENTIAL |  |  |  |  |  |  |

## References

[1]. Birkhoff And . C. Bartee, Modern Applied Algebra, Delhi: Mcgraw-Hill, 1970
[2]. N. Biswas, Logic Design Theory, India: Prentice Hall, 1993.

