Shunt compensator used for power quality improvement

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Abstract: This paper presents shunt compensator device to improve power quality which reduces the harmonic distortion and improves current maintaining the voltage unchanged. For this the shunt active power filter is used with the implementation of PI controller. In order to improve current, opposite harmonics are injected through the shunt active power filter and further it compared with the source current. This results improved power quality. Simulation results are presented to show that the power quality improvement using shunt active power filter.

Keywords: Active Power Filter, Power Quality, PI Controller

I. Introduction

The proliferation of microelectronics processors in a wide range of equipment’s, from home VCRs and digital clocks to automated industrial assembly lines and hospital diagnostics systems, has increased the vulnerability of such equipment to power quality problems. These problems include a variety of electrical disturbances, which may originate in several ways and have different effects on various kinds of sensitive loads. What were once considered minor variations in power, usually unnoticed in the operation of conventional equipment, may now bring whole factories to standstill. As a result of this vulnerability, increasing numbers of industrial and commercial facilities are trying to protect themselves by investing in more sophisticated equipment to improve power quality. Moreover, the proliferation of nonlinear loads with large rated power has increased the contamination level in voltages and currents waveforms, forcing to improve the compensation characteristics required to satisfy more stringent harmonics standard. Between the different technical options available to improve power quality, active power filters have proved to be an important alternative to compensate for current and voltage disturbances in power distribution systems. Different active power filters topologies have been presented in the technical literature and many of them are already available in the market. This paper will focus in the analysis of which to use with their compensation characteristics. Shunt active power filters schemes will be presented and analysed. The control scheme characteristics for shunt schemes will also be discussed. Finally, steady state and transient results for dynamic compensation, obtained from simulated and experimental setup will be presented.

II. Basic Configuration Of Active Power Filter

Shunt active power filter compensate current harmonics by injecting equal-but-opposite harmonic compensating current. In this case the shunt active power filter operates as a current source injecting the harmonic components generated by the load but phase shifted by 180°. This principle is applicable to any type of load considered a harmonic source. Moreover, with an appropriate control scheme, the active power filter can also compensate the load power factor. In this way, the power distribution system sees the nonlinear load and the active power filter as an ideal resistor. The current compensation characteristic of the shunt active power filter is shown in fig.

![Fig. (1) Basic shunt active power filter](image_url)
III. Principle Of Shunt Active Power Filter

Harmonics contamination is a serious and a harmful problem in Electric Power System. Active Power filtering constitutes one of the most effective proposed solutions. A shunt active power filter that achieves low current total harmonic distortion (THD), reactive power compensation and power factor correction is presented. Hence, it is necessary to reduce the dominant harmonics below 5%. Harmonic Amplification is one the most serious problem. It is caused by harmonic resonance between line inductance and power factor correction capacitors installed by consumers. Active filters for damping out harmonic resonance in industrial and utility power distribution systems have been researched. Traditionally based, passive L-C filters were used to eliminate line harmonics. However, the passive filters have the demerits of fixed compensation, bulkiness and occurrence of resonance with other elements. The recent advances in power semiconductor devices have resulted in the development of Active Power Filters for harmonic suppression. Various topologies of active filters have been proposed for harmonic mitigation. The shunt APF based on Voltage Source Inverter (VSI) structure is an attractive solution to harmonic current problems. The SAF is a pulse width modulated (PWM) VSI that is connected in parallel with the load. It has the capability to inject harmonic current into the AC system with the same amplitude but opposite phase than that of the load. The principal components of the APF are the VSI, a DC energy storage device that in this case is capacitor, a coupling transformer and the associated control circuits. The performance of an active filter depends mainly on the technique used to compute the reference current and the control method used to inject the desired compensation current into the line.

IV. Modelling And Control Description

1. Control Techniques:

There are several methods used to determine the harmonic current references. In this study, we apply two methods, the notch filter and the synchronous reference theory

A. Notch Filter Method:
In this type of control the load current is filtered by a cut band filter which is sometimes called filter “notch”. These cut band filters eliminate the fundamental component while letting the harmonic components. They therefore, the same cut frequency. In our study we have found that we can use a simple cut band filter but it does not have good insulation property. For that reason we have applied the method of notch filter which consists of two identical band pass filters in series, as shown in figure.

![Fig (2) The band pass filter algorithm](image)

B. Synchronous Reference Method:
This method involves transforming the coordinated a-b-c of the current in coordinated d – q and the assistance of the transforms of Park by setting the frequency thereof in synchrony with the network.

\[
\begin{bmatrix}
    id \\
    ig
\end{bmatrix}
= \frac{2}{3}
\begin{bmatrix}
    \sin(wt)\sin(wt - \frac{2\pi}{3}) & \sin(wt + 2\pi/3) \\
    \cos(wt)\cos(wt - \frac{2\pi}{3}) & \cos(wt + 2\pi/3)
\end{bmatrix}
\begin{bmatrix}
    ia \\
    ib \\
    ic
\end{bmatrix} = \begin{bmatrix}
    \frac{2}{3}\sin(wt) \\
    \frac{2}{3}\cos(wt)
\end{bmatrix} \begin{bmatrix}
    id \\
    ig
\end{bmatrix}
\]

The park transformation transforms the fundamental current component to continuous component while the harmonic current components undergo a shift in the frequency spectrum, the continuous component can be eliminated by adding a high pass filter (HPF). Harmonic currents references can be obtained by performing the inverse transform of parks synchronized with network frequency as shown in figure.3
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Fig. (3) Synchronous reference algorithm

\[ Tf = \frac{K \cdot B \cdot F}{P^2 + B \cdot P + WC^2} \]

Where:
- \( K \) is the gain,
- \( P \) is the Laplace operator,
- \( B \) is an angular frequency equal to \( 2\pi f_b \),
- \( f_b \) is the width of the busy band,
- \( \omega_c \) is the cut-off frequency.

C. DC Voltage Control:
There are several current control strategies proposed in the literature, namely, PI control, Average Current Mode Control, Sliding Mode Control and hysteresis control. Among the various current control techniques, hysteresis control is the most popular one for active power filter applications. Hysteresis current control is a method of controlling a voltage source inverter so that the output current is generated which follows a reference current waveform in this project. Generally, PI controller is used to control the DC bus voltage of SAF. The PI controller based approach requires precise linear mathematical model. This project basically deals with the modelling and design of shunt active power filter for compensation of harmonics and reactive power. Designs of different parameters like power circuit, control circuit, control strategies, EMI/ripple factor are discussed shunt active power filter is controlled to draw or supply a compensating current. This compensating current injected by the active filter makes the supply current sinusoidal. In this manner, a shunt active filter can be used to eliminate the current harmonics in the system.

D. PI Controller
The figure 4 shows the PI control scheme used in this paper, when the DC capacitor voltage \( V_c \) is sensed and compared with the reference value \( V_c^* \). The input of PI controller is the value of error, \( e = V_c^* - V_c \), and its output after a limit is considered as the magnitude of peak reference current \( I_{\text{max}} \). The coefficients of PI controller, \( K_P \) and \( K_I \), are fixed in our model to give a good performance dynamic for this active power filter.

E. Current Control
In this paper we have used the hysteresis current control; it’s very commonly used because of its simplicity of implementation and it’s rugged. This strategy provides satisfactory control of current without requiring extensive knowledge of control system model or its parameters. Figure 3.15 presents the principle of
command that this is mainly to maintain each of the currents generated by the APF’s in a band surrounding the reference currents.

![Diagram of Hysteresis current controller](image)

**Fig. (5) Hysteresis current controller**

Each violation of this band gives an order of commutation as the figure. 4 shows

![Hysteresis band curve](image)

**Fig (6) Hysteresis band curve**

V. Simulation Result And Discussion

In this paper the simulation results are shown for the two conditions such as:

(a) Performance of system without using shunt active power filter

(b) Performance of system with shunt active power filter

(a) **Without shunt APF**

Model and simulation result for a without use shunt active power filter, shown in the figure.

![Simulink Model without shunt APF](image)

**Fig. (7) Simulink Model without shunt APF**
(b) Use shunt active power filter
Model and simulation result for a with shunt active power filter are shown in the figure.
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Fig. (11) Source current before and after compensation

The waveform shown in fig. 11 demonstrates that source voltage, source current before and after compensation and filter current for phase A.

Fig (12) capacitor voltage and capacitor current

The waveform shown in fig. 12 demonstrates that capacitor voltage settles at nearly constant value of Vdc ref. and capacitor current settles at almost equal to the value of filter current.

Fig. (13) FFT analysis for load current

The performance of the system improves and the THD is reduced up to very large extent. Also, it is seen from the simulation results that the source current and the source voltages are in same phase i.e. the input power factor is unity and there is no reactive power from the source.
VI. Conclusion

In this paper we have studied the shunt active power filter with PI controller for power quality improvement. The power quality problems, circuit topology and operating principle of proposed shunt compensator are analysed in detail and some of the power quality problems which can be solved by shunt compensator are also discussed in the paper. Finally a Matlab/Simulink based model is developed and simulation results are present.

It was also observed that the location where the Shunt active power filter is connected in the electrical system which is very important for improvement of overall system dynamics performance. Shunt active power filter compensator will become a good solution for power quality improvement.

References