Low Power Energy Harvesting & Supercapacitor Storage

Manish Anand¹, A. M. Nagaraj²

¹(*M Tech Student, Department of EEE, Dayananda Sagar College of Engineering/ VTU, India*) ²(Associate Professor, Department of EEE, Dayananda Sagar College of Engineering/ VTU, India)

Abstract : This paper presents a hand cranked generator or an exercise bicycle to generate small amount of energy which is effectively stored in a supercapacitor Bank. Supercapacitors can absorb and deliver huge bursts of power in a short span of time owing to its low ESR (Equivalent Series Resistance). If more charging current is given to the battery it may lead to excessive heating and decreases the life of the battery but the charging current limit of supercapacitors is very high. Thus it can be charged rapidly which is a very useful feature for energy harvesting applications. The energy generated by hand cranking or coupling the generator with an exercise bicycle rapidly charges the supercapacitor bank via a current limiter. The supercapacitor Bank can then be effectively discharged using a low voltage boost converter to store the energy in a battery or to power small devices. This way the energy generated by the exercise bicycle which is an ambient source of energy can be effectively utilized which would have otherwise gone wasted.

Keywords - Energy Harvesting; energy storage, charge balancing, supercapacitors, DC-DC converters.

I. INTRODUCTION

Powering small electronic gadgets in remote area can be a challenging task. In some villages which are far away from electric grid people still use kerosene for lighting their lamps. Instead some form of energy harvesting device can be used to produce small amount of power which will be sufficient to power small devices like radio, LED lamps & cell phone charging.



Energy harvesting devices capture excess energy from everyday movements. They capture short burst of intermittent flow of energy [1]. This project aims to make an energy harvesting arrangement that can be cranked for few minutes to generate enough energy to power small devices. Exercise bicycle can be used as an ambient source of energy [3]. It can easily generate 20-30W of power if proper arrangement is made. Some sort of storage device is needed which absorbs the energy generated in short span of time which can be utilized gradually in a relatively longer time span. So some energy storage device is needed which quickly absorbs the intermittent flow of energy. Thus supercapacitors are being used as energy storage device. A supercapacitor is a capacitor which can store large quantity of charge and has capacitance in order of thousands of Farads. Supercapacitor has a typical time constant of approximately one second. One time constant reflects the time necessary to charge a capacitor 63.2% of full charge or discharge to 36.8% of full charge.

When the supercapacitor bank discharges while utilizing the stored energy its voltage drops. Most of the loads work at constant voltage. So a DC-DC converter is required to give a stable voltage output to power loads i.e. LEDs, cellphone charging etc.

Care has to be taken regarding the supercapacitor charging current. Some form of arrangement is needed to regulate the charging current flow as the supercapacitor Bank behaves at dead short at uncharged state.

The energy stored in the supercapacitor Bank has to be utilized soon after storing it due to higher leakage current of supercapacitor than the battery. So we must generate the energy just before using it. Otherwise it can be transferred to the battery if we want to store it for longer time.

When connecting the supercapacitors in series a charge balancing circuit is used to evenly distribute the charge across the series connected capacitors and save them from overvoltage condition which might deteriorate their lifetime.

Supercapacitor as energy storage medium is an emerging field. Research is going on to increase its Energy Density and reduce its leakage current. With the prices of supercapacitor decreasing in coming years due to popularity and improvement in technology, they can even replace batteries in certain applications.

II. **ENERGY HARVESTING SETUP**

Initially a small model using 3W DC generator has been made. Later this setup can be scaled up using bicycle power generator which will generate 20 to 30W power. The energy harvesting & supercapacitor storage setup used is very versatile. Although a hand cranked generator is being used as the energy generating device, the same setup can be used for recovering regenerative braking energy from electric vehicle. These are the following components as shown in Fig.1:

- Hand Cranked Generator
- Current Limiter
- Voltage Sense Circuit
- Supercapacitor Bank
- Charge Balancing Circuit
- **DC-DC** Convertor
- Load

2.1 Hand Cranked Generator

A small PMDC motor is used a hand cranked generator using proper gear or pulley arrangement to rotate it at higher speed even when the cranking speed is low. PMDC generator is a very good choice for energy harvesting applications as it does not require field supply [4]. A 12V DC generator with power output of 2-3W is used. Voltage Vs. speed graph helps to predict the input voltage range going to the DC-DC converter.



2.2 Current Limiter

Supercapacitor bank will behave as a short circuit to the hand cranked generator when in uncharged or low charge state. So a current limiting arrangement is required which will offer some resistance to the current flow. Here LM317 based current limiter has been used. In this particular configuration the current flow is limited to 250mA. The current flow can be programmed from 0-1.25A using suitable resistor value. : Iout=1.25/R1 (1)

Since some work is done to rotate the hand cranked generator, if the current draw is higher for the Supercapacitor Bank charging, more work has to be done i.e. more torque (Torque=Force*Distance) has to

applied to rotate the hand cranked generator .So by adjusting the current flow in the supercapacitor bank we can adjust the torque needed to rotate the hand cranked generator.



Fig.3. current limiter

2.3 Voltage Sense Circuit

It disconnects the supercapacitor bank from the hand cranked generator when it is charged to set voltage and prevents overcharging. It has been made using single supply dual comparator LM 393. One comparator disconnects the charging when the supercapacitor bank voltage reaches a preset level programmed by the preset R6. Other comparator is used to give the supercapacitor bank voltage indication.



Fig.4 voltage sense circuit

2.4 Supercapacitor Bank

Supercapacitor Bank is made by connecting supercapacitors in series. Each individual supercapacitor is rated for 2.7V. So for increasing the voltage rating they need to be connected in series. Sizing of supercapacitor Bank is an important aspect. It depends on how much energy need to be stored. Three 100F supercapacitors in series have been connected to make 33.33F/8V supercapacitor Bank which will store energy given by $: E = \frac{1}{2} C V^2 = \frac{1}{2} [33.33] [7.5]^2 = 937.4$ Joules (2)

2.5 Charge Balancing Circuit

A charge balancing circuit is needed to avoid any single capacitor in the supercapacitor Bank to be overcharged [2]. While charging slight voltage difference will be there between the individual supercapacitors due to slightly different parameters such as internal resistance even for same part number, when they are connected in series. An active charge balancing circuit has been used. It consists of a comparator controlled switch which bypasses small amount of current when one supercapacitor is slight more charged than the other to equalise the charge distribution.

2.6 DC-DC Converter

The supercapacitor bank voltage will drop gradually as it will discharge but a constant output voltage is required for powering the loads. So a buck converter based on MC34063 IC is used. The buck converter has been designed using the design formulae given in the datasheet.

The utilization of power from the supercapacitor Bank depends on the input voltage range of the DC-DC converter. Since the energy in the capacitor is proportional to the voltage squared according to : $E = \frac{1}{2} C V^2$ (3)

It is possible to utilize approximately 75% of the available energy if the application utilizes from the rated voltage to $\frac{1}{2}$ rated voltage of the capacitor.



Low ESR electrolytic capacitors C1 & C3 have been used to minimize the losses in the DC-DC converter. R1 is used for current limit in case of short circuit at the load side. The output voltage can be varied by altering the arrangement of voltage sense potential divider made by R2 & R3.

Some other ASICs (Application Specific ICs) like LM2576, LM2596, and LM2577 also can be used to make DC-DC converters with added features like soft start, overcurrent protection, shutdown etc.

Linear Technology offers a lot of alternatives for energy harvesting applications with very high efficiency.

PCB designing for the DC-DC converters is done using EAGLE PCB design software. Proper layout technique is used like keeping the trace of freewheeling diode, input and output capacitors as short as possible. All the high current path traces are made thicker than the rest.

2.7 Load

LED load is connected to DC-DC converter. Dimmer circuit is developed using TLC555 IC which is a CMOS version of 555 Timer and can achieve very high frequencies till 1.2 MHz. This ensures flicker free dimming of white LEDs. For higher frequency applications >50 kHz, TIP31C can be replaced with a suitable rating MOSFET and a MOSFET driver.



Fig.6 LED PWM brightness controller



Another load is cell-phone whose charging requires the output voltage of the buck converter to be set at 5V. There are other hand cranked chargers available in the market but the advantage of this arrangement is that it will charge the cell-phone even for some time after the cranking stops because of the stored energy in the supercapacitors. The previously available chargers only charge during the cranking time with lesser efficiency.

III. Simulation

Simulation of Buck Converter using MC34063 Switching Regulator IC has been done in LT Spice. LT Spice is free Spice simulation software from Linear Technology which was previously called switcher CAD. It provides schematic capture and waveform viewer. Component library mostly contains Linear Technology but even a 3rd party model can be imported. LT Spice IV netlist can be imported into any PCB design software.



The output voltage is 5V as expected with an input voltage range of 6-12V. The output voltage ripple is 100mV. The current ripple in the inductor is 700mA. The simulation gives an idea of the voltage range of the supercapacitor bank which can be utilised by the DC-DC converter for powering any load at steady voltage.

IV. CONCLUSION

The paper demonstrates an arrangement for low power energy harvesting and storage device for powering small electronic gadgets. The energy absorbing ability of this arrangement is much better than battery based energy storage systems in energy harvesting. The arrangement can be used for off grid applications in remote areas. This energy harvesting arrangement does not depend on any intermittent sources like wind or sunlight. The energy stored must be utilized soon due to high leakage current of supercapacitors. If energy has to be stored for longer duration then it must be transferred to a battery using a DC-DC converter. Most electronics have a minimum voltage threshold for utilization, limiting the effective utilization voltage of the supercapacitor although there is no limitation in the supercapacitor itself. So, better utilization of the stored energy can be done by using low voltage DC-DC converters. Future work includes developing regenerative brakes for bicycle for urban use where frequent braking is required. So, whenever brakes are applied on a busy road the energy is recovered back in the supercapacitor bank instead of getting wasted in the brakes.

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